

Chief Patron**Dr. P. Jaya Rami Reddy, Director****Patron****Dr. B. Sreenivasa Reddy, Principal****Chairman****Dr. K. Suresh Reddy, Prof. & HOD, ECE Dept.****Convener****Dr. M.V.R. Vittal, Associate Professor of ECE****Technical Advisors****Dr. S. Nagaraja Rao, Professor****Dr. P. Abdul Khayum, Professor****Organizing Secretaries****Smt. K.Udaya Kiran, Asst. Professor****Sri. D.V. Srihari Babu, Asst. Professor****Smt. C. Rajeswari, Asst Professor****Sri D. Sharath Babu Rao, Asst. Professor****Smt. M. Chaithanya, Asst. Professor****Sri. T. Naresh, Asst. Professor****Sri G. Pavan Kumar, Asst. Professor****Members****Dr. R. Sudheer Babu, Asst. Professor****Sri G. Amjad Khan, "****Dr. S.M. Shamsheer Daula, "****Dr. S. Vyshali, "****Sri B. Siva Reddy, "****Sri. G. Ramesh, "****Smt. C. Rajeswari, "****Sri K. Raju, "****Smt. T. Swathi, "****Sri D. Sarath Babu Rao, "****Sri A. Venkata Subba Reddy, "****Sri K. Naga Koushil Reddy, "****Ms. W. Yasmeen, "****Smt. G. Divya Praneetha, "****A One-Day National Level Workshop on
"VLSI Fabrication Principles and FPGA Design
Flow"****(10th January, 2019)****Organized by****Department of ECE****G. Pulla Reddy Engineering College****(Autonomous):KURNOOL-518007****Andhra Pradesh, India****Accredited By NAAC of UGC and NBA of AICTE****E-mail: vittalmvr.ece@gprec.ac.in****www.gprec.ac.in**

About The College

The College is the brainchild of the renowned philanthropist, Sri G. Pulla Reddy. Established in 1985 and managed by G. Pulla Reddy Charities Trust, the college is affiliated to J.N.T. University, Ananthapuramu and approved by AICTE, accredited by NBA of AICTE and NAAC of UGC. The college offers under Graduate Programs in Five Engineering branches and Seven Post Graduate courses. The college is situated on Kurnool – Chittoor National Highway No.18. Kurnool is well connected by Rail and Road. The places of interest nearby are Srisailam, Alampur, Mahanandi, Mantralayam and Belum Caves.

About The Department

The department of Electronics and communication Engineering was started in July 1985. The department has excellent infrastructure facilities, laboratories and experienced faculty. In addition, the department offers PG programs in Communications and Signal Processing & VLSI & Embedded Systems. The department has MOU with CRANES Software International Ltd. Hyderabad, CoreEL Technologies, Cadence Tools, Xilinx Tools, Xilinx 7-series FPGA boards. Intel Atom Processors & Licensed Software

About The Workshop

Field Programmable Gate Arrays(FPGA) are bridging the gap between single functioned Application Specific Integrated Circuits(ASIC) and software execution capable General Purpose Processors (GPP). In this workshop, we are covering the FPGA fundamentals from architecture to programming, especially related to Xilinx FPGAs. Laboratory sessions will be done on Xilinx 7-series FPGAs using Xilinx Vivado IDE. Since the grasp of modern VLSI Fabrication Principle enrich the hardware engineer's scope for error free, low power, high reliable and testable design, such fabrication ideas will be covered.

- Fabrication Trends
- Fabrication Principles & Trends
- Fabrication Economics
- FPGA Working
- FPGA Design Flow
- FPGA Design Tools
- Xilinx 7-Series FPGA
- FPGA Debugging

The Resource Persons

The experienced team of two members from G. Pulla Reddy Engineering College (Autonomous), KURNOOL, AP professional design and teaching average experience of over 20 years in the field of VLSI. Namely, Dr. G. Eranna, Professor OF ECE, formerly, Scientist-F at CSIR will cover the Microelectronics part of the workshop. While Sri. T Thammi Reddy, Associate Professor in ECE will cover the Full FPGA flow.

Intended Participants

The workshop is open to Faculty Members, PG Students & Research scholars of ECE, EEE & CSE streams of Engineering.

Facilities for Participants

Participants will be provided working lunch, Tea & Snacks. The participants are requested to make their own arrangements for Lodging. No TA will be paid

Registration

Registration for the workshop can be done by Enrolling your details at the below website.

<https://goo.gl/forms/ZBvuS9tcIEp6aXHx1>

Eligible candidates are informed by e-mail. They can pay the registration fee of Rs.500 on the day of workshop. As the number of seats are limited and the selection is based on first come, first serve basis.

Important Dates

08-01-19 Last Date for Submission of Application

09-01-19 Intimation of selection to participants through E-mail

Address for all Correspondence

Dr. M.V.R Vittal, Convener
Department of Electronics and Communication Engineering

G. Pulla Reddy Engineering College (Autonomous)
KURNOOL - 518 007. Andhra Pradesh.

Ph: 08518 - 270957(O), Fax: 08518 270957

Mobile: +91-9110383160

E-mail: vittalmvr.ece@gprec.ac.in

A One Day National Level Workshop on VLSI Fabrication Principles and FPGA Design Flow (VFPFD-19)

(10th January, 2019)

**G. Pulla Reddy Engineering College (Autonomous)
Kurnool**

REGISTRATION FORM

(Use Xerox copy if necessary)

I. Name :

2. Designation :

3. Organization :

4. Address for communication:

Phone with STD code Office:

Mobile:

E-mail:

5. No Accommodation & TA will be provided

Signature of the Applicant

Sponsorship

Mr/Ms/Mrs/Dr_____is an employee of our institute/ industry and is sponsored to attend the One day National level Workshop on "VLSI Fabrication Principles and FPGA Design Flow"

Place:

Date :

Signature & Seal of Sponsoring
Authority

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous): KURNOOL

Department of Computer Science & Engineering

Circular

Date: 21.11.2018

All the technical staff of CSE department is informed that, CSE department is organizing a one day workshop on "Hebeon" on 22.11.2018 at E-class room from 10.00 A.M onwards. Hence all are requested to attend the workshop and get benefited.


HOD

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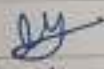

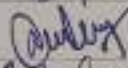
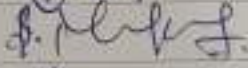

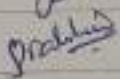
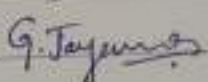
Department of Computer Science & Engineering

HEBEON WORKSHOP

Attendance Sheet

Date: 22.11.2018

The following staff has participated in the Hebeon workshop on 22.11.2018 organized by Computer Science and Engineering department of G Pulla Reddy Engineering College:

S.No	Emp. ID	Name of the Technical Staff	Signature
1	9058	Sri.I Suresh	
2	9125	Smt.G Mayadevi	
3	9133	Sri. E Arun Kumar	
4	9187	Smt. S Meena Kumari	
5	9188	Sri. Y Vasudeva Reddy	
6	9193	Sri. P Prahallad Kumar	
7	9199	Smt.G. Jayamma	

G.PULLA REDDY ENGINEERING COLLEGE (Autonomous): KURNOOL.

Department of Computer Science & Engineering

HEBEON WORKSHOP

Resource Person Details

Date: 22.11.2018

Resource person for the HEBEON workshop is Mr. M. Imtiazur Rahman, Associate Professor, CSE department.

G. Pulla Reddy Engineering College (Autonomous): Kurnool

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Department of Computer Science and Engineering



WORKSHOP ON HEBEON

This certificate is issued to **Sri.I.SURESH** of **G.Pulla Reddy Engineering College, Kurnool** for attending One Week Online Training Program on "HEBEON" conducted by Department of CSE, GPREC on 22nd November, 2018.

Sri. M.Imtiaz ur rahaman
Convenor

Dr. N. Kasi Viswanath
HOD

Dr. B. Sreenivasa Reddy
Principal

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Department of Computer Science and Engineering



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Sri. M.Imtiaz ur Rahaman
Convenor

Dr. N. Kasi Viswanath
HOD

Dr. B. Sreenivasa Reddy
Principal

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Sri. M.Imtiaz ur Rahaman
Convenor

Dr. N. Kasi Viswanath
HOD

Dr. B. Sreenivasa Reddy
Principal

Department of CSE

Circular

Date: 23-Oct-2018

All the Lab Engineers, H/w Engineers, Programmers and Computer operators of CSE dept. are hereby informed that there will be a Skill Development Program sessions on usage of E-Box tool and also on Networking devices as per the following schedule.

Date/Time	Training Module	Resource Persons	Venue
25 th Oct 2018 from 9:45AM	E-Box Tool	A.Vishnu Vardhan Reddy & K.V.Rameshwar Reddy	R & D Lab
26 th Oct 2018 from 9:45AM	Networking	Dr.G.Vijaya Kumar	CISCO Lab

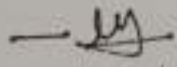
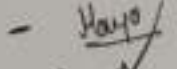
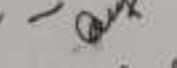
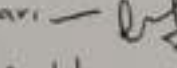
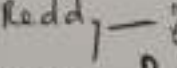


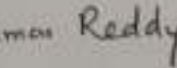
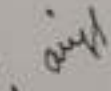
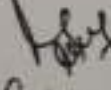
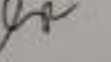
Hence all the above members are required to attend the same without fail.

Copy to : Circulate to all the technical staff

Circulate to all the resource persons

Circulate to all faculty


HOD

- (1) I. Suvenh — 
 - (2) G. Mayadevi — 
 - (3) E. Arun Kumar — 
 - (4) S. Meena Kumari — 
 - (5) Y. Vasudeva Reddy — 
 - (6) P. Prahalad Kumar —  P. Prahalad
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-
- (1) A. Vishnu vardhan Reddy 
 - (2) K.V. Rameshwar Reddy 
 - (3) Dr. G. Vijaya Kumar 

G. PULLA REDDY ENGINEERING COLLEGE (Autonomous); KURNOOL

Department of Computer Science & Engineering

Skill Development program on E-Box usage

Resource Persons : 1. A. VISHNU VARDHAR REDDY
2. R. V. RAMESWAR REDDY

Date : 25/10/2018

S.No	Name of the staff	Signature
1	I. SURESH	
2	Y. VASUDEVA REDDY	
3	G. Mayadevi	
4	S. Meena Kumari	
5	G. Jayamma	
6	P. Prabhakar Kumar	
7	E. Arun Kumar	

HOD



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This certificate is issued to **Sri.I.Suresh** of **G. Pulla Reddy Engineering College, Kurnool** for attending one day Skill Development Program session on "Usage of E-Box tool" conducted by Department of CSE, GPREC on 25th October 2018.

Sri. A. Vishnuvardhan Reddy

Resource Person

Dr. N. Kasi Viswanath

HOD

Dr. B. Sreenivasa Reddy

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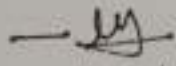
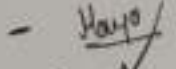
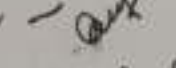
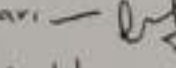
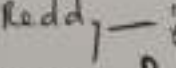
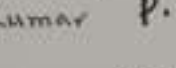

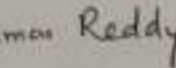
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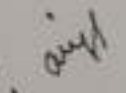
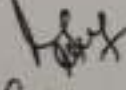
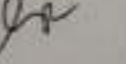
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This certificate is issued to **Sri.I.Suresh** of **G. Pulla Reddy Engineering College, Kurnool** for attending one day Skill Development Program session on "Networking Devices" conducted by Department of CSE, GPREC on 26th October 2018.

Dr.G.Vijaya Kumar

Resource Person

Dr. N. Kasi Viswanath

HOD

Dr.B.Sreenivasa Reddy

Principal

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Dr.G.Vijaya Kumar

Resource Person

Dr. N. Kasi Viswanath

HOD

Dr.B.Sreenivasa Reddy

Principal

G. Pulla Reddy Engineering College (Autonomous): Kurnool

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Dr.B.Sreenivasa Reddy

Principal

Dr. G. Eranna, Professor OF ECE, formerly, Scientist-F at CSIR
Sri. T Thammi Reddy, Associate Professor in ECE

Field Programmable Gate Arrays (FPGA) are bridging the gap between single functioned Application Specific Integrated Circuits (ASIC) and software execution capable General-Purpose Processors (GPP). In this workshop, we are covering the FPGA fundamentals from architecture to programming, especially related to Xilinx FPGAs. Laboratory sessions will be done on Xilinx 7-series FPGAs using Xilinx Vivado IDE. Since the grasp of modern VLSI Fabrication Principle enrich the hardware engineer's scope for error free, low power, high reliable and testable design, such fabrication ideas will be covered.

